

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10016898	12/14/2001	365	5	2818	VOH4

**\*\*APPLICANTS:** Tsao Hsing-Ya; Lee Peter; Hsu Fu Chang.

**\*\*CONTINUING DATA VERIFIED:**  
THIS APPLN CLAIMS BENEFIT OF 60/277,852 03/22/2001

**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		ATTORNEY DOCKET NO.
35 USC 119 exceptions: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		4P01 008
Verified and Acknowledged Examiners: n/a		
TITLE : Two transistor flash memory cell for use in EEPROM arrays with a programmable logic device		

NOTICE OF ALLOWANCE MAILED	Assistant Examiner	CLAIMS ALLOWED	
ISSUE FEE		Total Claims	Print Claim for O.G.
		DRAWING	
		Figs Drawn	

☐ **TERMINAL  
DISCLAIMER**

**PREPARED FOR ISSUE**

Application Examiner

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